

Abstract of the Disclosure

In matrix array substrate for flat-panel display devices or the like in which a repair circuit for connecting a defected 5 pixel electrode and next electrode in a tandem is disposed between the two pixel electrodes, a contact hole for connecting one of connector electrodes of the repair circuit to one of the pixel electrodes is disposed within contours of a scanning line. Meanwhile, in a matrix array substrate for normally white 10 mode flat-panel display devices, an island metal pattern is disposed within the contours of a scanning line and within a storage-capacity-forming portion extended from one of the two electrodes. The island metal pattern is formed simultaneously with signal line and has a scanning-line-wise size larger than 15 width of the scanning line.